

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	37	(battery with cover with (pivotally or rotat\$4 or pivot) with (slidably or slidingly))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 13:53
L2	1695	84/726,743,267,733,727,418.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:11
L3	34	2 and (amplif\$7 or preamplif\$7) with (housing or compartment)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:28
L4	3	(circuit adj board\$1 or chip\$1) with spacer with (guide adj rail)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:12
L5	726	(circuit adj board\$1 or chip\$1) with (guide adj rail)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:25
L6	7	5 and mounting adj rail	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:14
L7	1	"84"/\$.ccls. and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:38
L8	184	5 and mounting with (circuit near5 board\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:14

L9	1135	(circuit adj board\$1 or chip\$1) with ((guide or mount\$3) near4 rail)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:27
L10	2	9 and (amplif\$6 or preamplif\$7) and "84"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:26
L11	664	(circuit adj board\$1 or chip\$1) near5 ((guide or mount\$3) near4 rail)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:29
L12	1	11 and (amplif\$7 or preamplif\$7) with (housing or compartment)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:28
L13	0	11 and "84"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:28
L14	8	11 and "84"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:28
L15	88	(circuit adj board\$1 or chip\$1) near5 ((guide or mount\$3) near4 rail) with surface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:44
L16	420	(circuit adj board\$1 or chip\$1) near5 (guide adj rail)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:34

L17	1	"84"/\$.ccls. and 16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:33
L18	29	(circuit adj board\$1 or chip\$1) near5 (mounting adj rail)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:34
L19	4	("6033264").URPN.	USPAT	OR	OFF	2005/07/05 14:36
L20	2	("4322120"   "5716241").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/05 14:36
L21	1	"84"/\$.ccls. and (circuit adj boards) with rail	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:38
L22	12	"84"/\$.ccls. and (circuit adj boards) with mounting	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:44
L23	15	(circuit adj board\$1 or chip\$1) near5 ((guide or mount\$3) near4 rail) with sliding\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:46
L24	15	(circuit adj board\$1 or chip\$1) near5 ((guide or mount\$3) near4 rail) with sliding\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:46
L25	11	(circuit adj board\$1 or chip\$1) near5 ((guide or mount\$3) near4 track\$1) with sliding\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 14:46
S1	14	((("4351217") or ("4532847") or ("5689082") or ("5693898") or ("5010802") or ("5837912") or ("5929362"))).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 13:53

S2	9	S1 and amplif\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:24
S3	9	S1 and (amplif\$4 or preamplif\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:49
S4	1	S1 and (preamplif\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:22
S5	1713	84/633,665,711,741,743,267.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:49
S6	800	S5 and (amplif\$7 or preamplif\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 16:00
S7	2	("6278059").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:58
S8	79	S6 and battery and (circuit\$2 or chip\$1) and (output\$4 or plug\$2 or jack)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 16:49
S9	162	housing near5 battery with cover near5 removable	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 15:26

S10	0	S9 and "84"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 15:26
S11	1	"84"/\$.ccls. and battery near5 cover near5 removable	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 15:31
S12	41	"84"/\$.ccls. and battery with cover	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 15:31
S13	24	"84"/\$.ccls. and battery near4 cover	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 15:31
S14	542	S6 and (circuit\$2 or chip\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 15:55
S15	25	S6 and (circuit\$2 or chip\$1) with housing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 15:55
S16	33	S5 and (amplif\$7 or preamplif\$7) with housing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/05 13:54
S17	4	S5 and (préamplif\$7) with housing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 16:05

S18	172	"84"/\$.ccls. and ((amplif\$7 or preamplif\$7) with jack with output)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 16:06
S19	26	"84"/\$.ccls. and ((amplif\$7 or preamplif\$7) with jack with output with cable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 16:06
S20	3	84/727.ccls. and (preamplif\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 16:50